

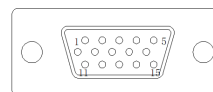
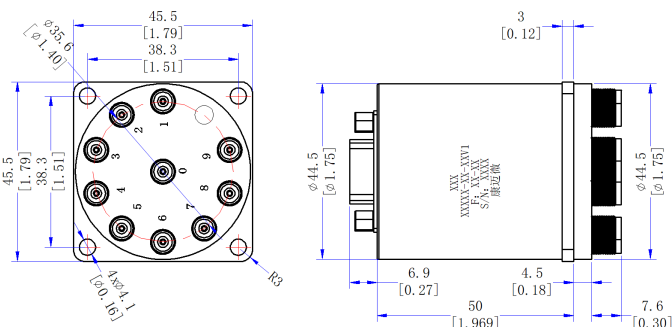
Tech specifications

Parameters	Min	Typical	Max	Units
Frequency	DC	~	18	GHz
IL			0.3	dB@6G
IL			0.5	dB@18G
Isolation	70			dB@6G
Isolation	55			dB@18G
VSWR			1.3	@6G
VSWR			1.5	@18G
Power Capacity			80	W@6G
Power Capacity			50	W@18G

Reference picture



Configuration(mm)



Other parameters

Switch way	SP9T
Control	TTL Control
Switching characteristic	Mechanical reflective switch
Switch time	15ms
Connector	SMA-K
Impedance	50Ω
Working voltage	+24V
Working current	150mA
Operation Temp.	-25~+65°C Design assurance
Storage Temp.	-55~+85°C Design assurance
Switching Lifetime	2 million Cycles
Control port	D-SUB15 Male
Switching sequence	First disconnect and then close

Control Pin Definition

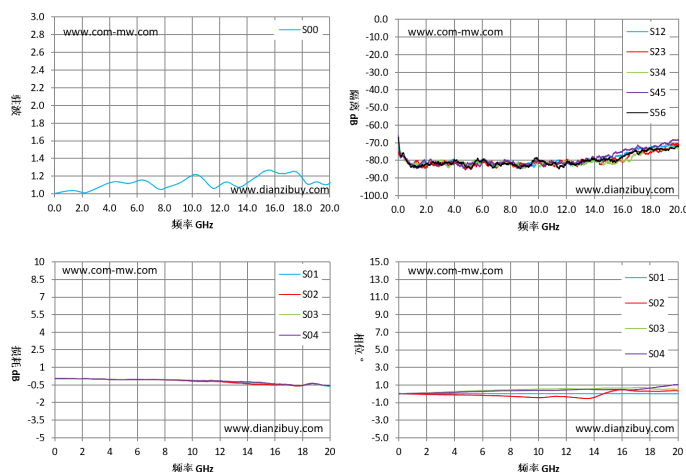
Pin 1	TTL high level (3~5V)	RF 0-1
Pin 2	TTL high level (3~5V)	RF 0-2
Pin 3	TTL high level (3~5V)	RF 0-3
Pin 4	TTL high level (3~5V)	RF 0-4
Pin 5	TTL high level (3~5V)	RF 0-5
Pin 6	TTL high level (3~5V)	RF 0-6
Pin 7	TTL high level (3~5V)	RF 0-7
Pin 8	TTL high level (3~5V)	RF 0-8
Pin 9	TTL high level (3~5V)	RF 0-9
Pin 11	+24V	/
Pin 12	GND	/
Pin 10,13~15	Not used	/

Note: The specifications and performance data contained in this data sheet are based on tests established by CMW.

Typical test data

Frequency(GHz)	S00 VSW R	S01 IL dB	S12 ISO dB	S02 Phas e
0.01	1.01	-0.01	-73.1 6	-0.03
1.43	1.04	-0.03	-83.4 1	-0.05
2.86	1.05	-0.04	-82.2 3	-0.11
4.29	1.14	-0.05	-82.1	-0.14
5.71	1.13	-0.04	-79.8 9	-0.17
7.14	1.1	-0.05	-82.3 5	-0.23
8.57	1.09	-0.08	-81.0 7	-0.34
10	1.21	-0.16	-79.7 5	-0.44
11.43	1.07	-0.15	-80.0 7	-0.31
12.86	1.11	-0.25	-81.6 4	-0.48
14.29	1.15	-0.29	-79.6 8	-0.24
15.71	1.27	-0.44	-77.2 4	0.42
17.14	1.24	-0.42	-73.7 8	0.3
18.57	1.11	-0.41	-72.0 9	0.28
20	1.12	-0.63	-70.9 1	0.33

Typical test curve



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