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\u4e3b\u8981\u53c2\u6570<\/h3>\r\n \r\n \r\n \r\n \r\n \r\n \r\n \r\n \r\n \r\n \r\n

\u53c2\u6570<\/th>\r\n	\/<\/th>	\u6700\u5c0f<\/th>\r\n	\u5178\u578b<\/th>\r\n	\u6700\u5927<\/th>\r\n	\u5355\u4f4d<\/th>\r\n <\/tr>\r\n <\/thead>\r\n
\u963b\u5e26\u9891\u7387<\/td>\r\n \r\n	<\/td>	1960<\/td>	~<\/td>\r\n	2040<\/td>\r\n	MHz<\/td>\r\n <\/tr>\r\n
\u963b\u5e26\u6291\u5236<\/td>\r\n \r\n	\u2265<\/td>	40<\/td>	<\/td>\r\n	<\/td>\r\n	dB<\/td>\r\n <\/tr>\r\n
\u901a\u5e26\u9891\u7387<\/td>\r\n \r\n	<\/td>	DC<\/td>	~<\/td>\r\n	1950<\/td>\r\n	MHz<\/td>\r\n <\/tr>\r\n
\u901a\u5e26\u9891\u7387<\/td>\r\n \r\n	<\/td>	2050<\/td>	~<\/td>\r\n	4000<\/td>\r\n	MHz<\/td>\r\n <\/tr>\r\n
\u901a\u5e26\u635f\u8017<\/td>\r\n \r\n	<\/td>	<\/td>	<\/td>\r\n	3.5<\/td>\r\n	dB<\/td>\r\n <\/tr>\r\n

Note: The specifications and performance data contained in this data sheet are based on tests established by CMW.

\u53c2\u6570<\/th>\r\n	\/<\/th>	\u6700\u5c0f<\/th>\r\n	\u5178\u578b<\/th>\r\n	\u6700\u5927<\/th>\r\n	\u5355\u4f4d<\/th>\r\n <\/tr>\r\n <\/thead>\r\n
\u901a\u5e26\u9a7b\u6ce2<\/td>\r\n \r\n	<\/td>	<\/td>	<\/td>\r\n	1.8<\/td>\r\n	<\/td>\r\n <\/tr>\r\n \r\n <\/tbody>\r\n <\/table>\r\n <\/div> **\u5176\u5b83\u53c2\u6570<\/h3>** \u63a5\u53e3\u5f62\u5f0f<\/td> SMA-K<\/td><\/tr> \u963b\u6297<\/td> 50\u03a9<\/td><\/tr> \u8868\u9762<\/td> \u9ed1\u8272\u70e4\u6f06<\/td><\/tr><\/tbody><\/table> <\/div>\r\n \r\n \r\n \r\n <\/div>\r\n \r\n \r\n **\u5916\u5f62\u7ed3\u6784\u56fe\uff08mm\uff09<\/h3>\r\n <\/p>** **<\/p><\/div>** **\u6ce8\u610f\u4e8b\u9879<\/h3>** 1<\/td> \u6e05\u6d17\u4ea7\u54c1\u8868\u9762\uff0c\u8bf7\u52ff\u5c06\u4ea7\u54c1\u6d78\u6ce1\u6e05\u6d17\u6db2\u4e2d\u6e05\u6d17\u3002<\/td><\/tr><\/tbody> <\/table><\/div>\r\n <\/div>\r\n <\/div>\r\n <\/div>\r\n "

Note: The specifications and performance data contained in this data sheet are based on tests established by CMW.